

म्हे र १ विते

FIG.2

```
shift register
        >output of PN code
           (K1 \sim K7)
1 \rightarrow 1 (K1)
1 \rightarrow 1 (K2)
0 \rightarrow 0 (K3)
                      K 7
                            K6
                                 K5 K4
                                               KЗ
                                                     K2 K1
   \rightarrow 1 (K4)
                             0
                                    0
0 \rightarrow 0 (K5)
0 \rightarrow 0 (K6)
1 \rightarrow 1 (K7)
```

FIG.3

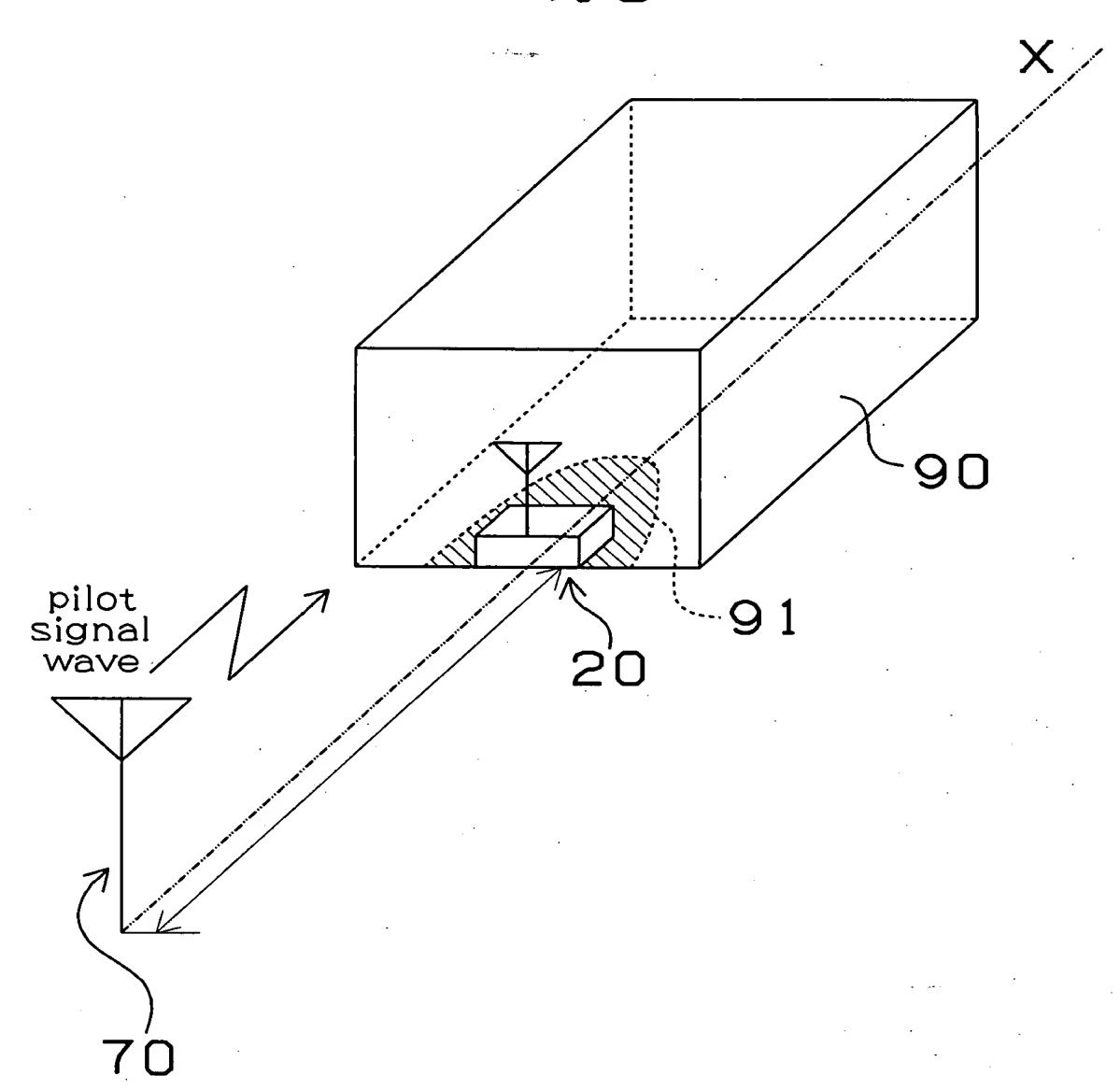


FIG.4

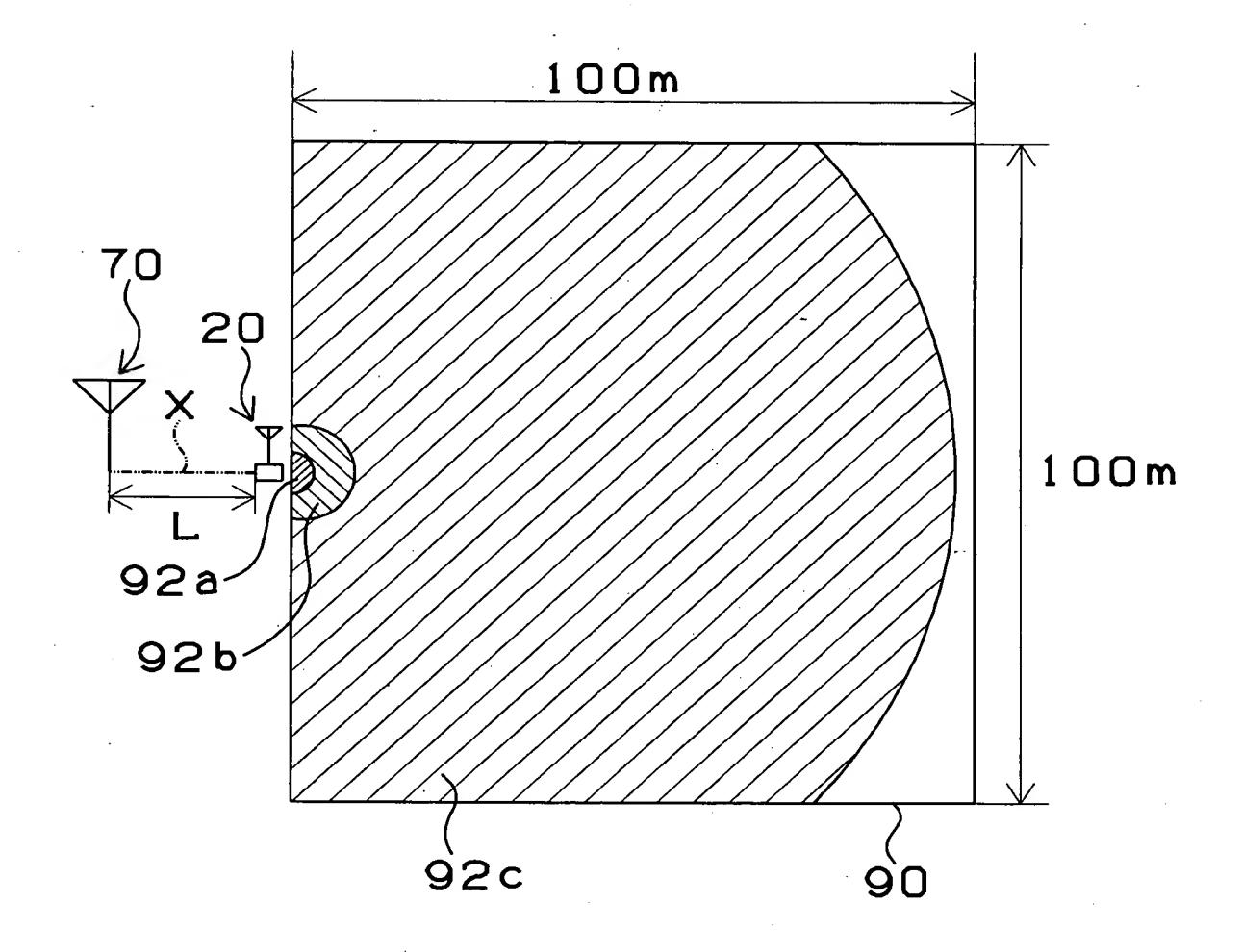


FIG.5

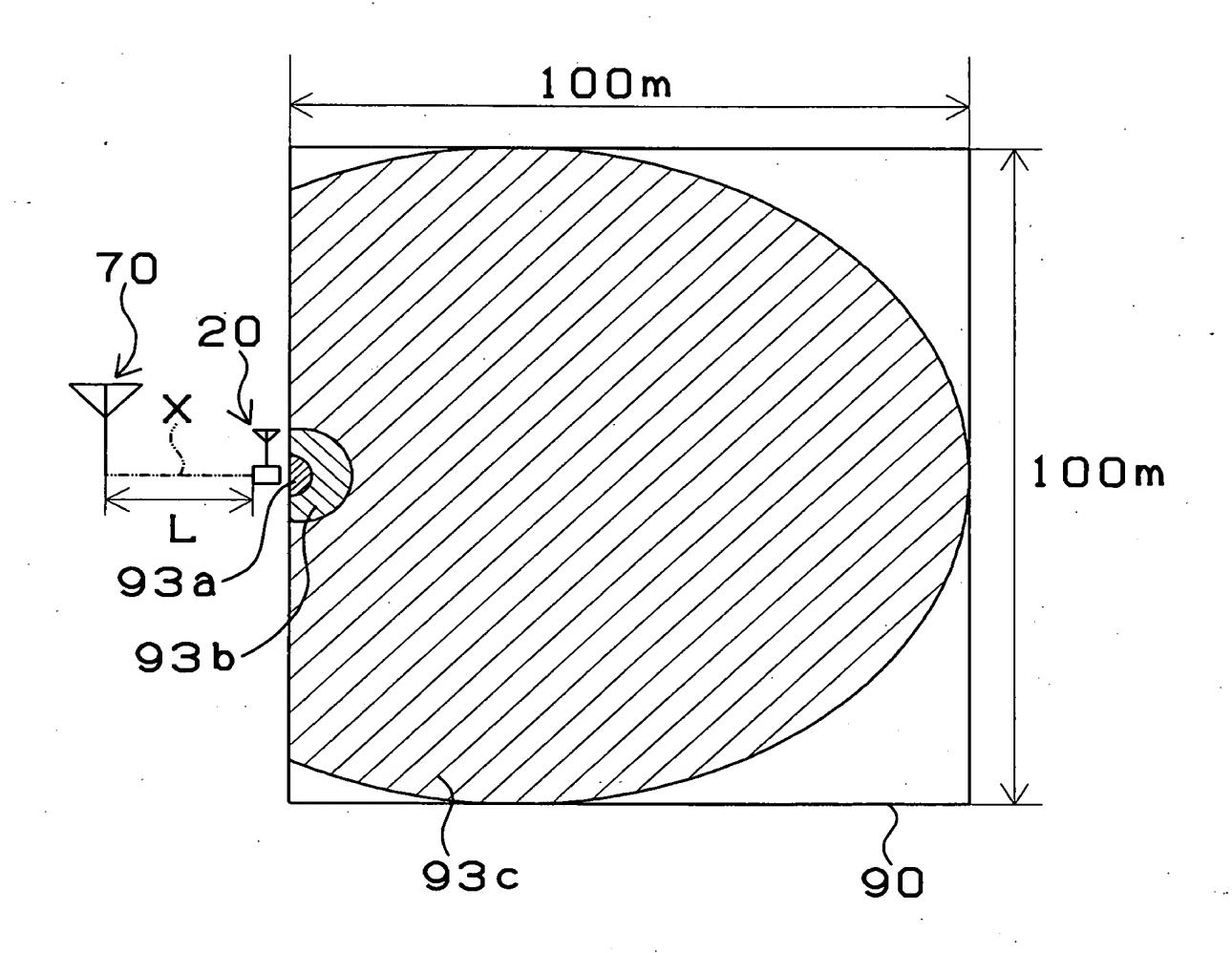


FIG. 6

